



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:
Alan R. Reinberg et al.

Serial No.: 09/740,256

Filed: December 19, 2000

For: METHOD OF FABRICATING
A CONDUCTIVE PATH
IN A SEMICONDUCTOR DEVICE

§ Group Art Unit: 2811
§
§
§ Examiner: Tran, T.
§
§ Atty. Docket: MCRO:106-2
§ 95-0412.02
§

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313

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37 C.F.R. 1.8

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Date

Michael G. Fletcher

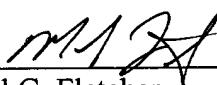
Sir:

TRANSMITTAL OF CORRECTED FORMAL DRAWINGS

On June 1, 2004, Applicants submitted formal drawings along with payment of the issue and publication fees for the above-referenced application. However, it appears that the transmitted formal drawings did not include certain amendments previously authorized by the Examiner. Accordingly, please find enclosed corrected formal drawings that include such amendments..

Respectfully submitted,

Date: June 4, 2004


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